



PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Takaki YOSHIDA et al.

Group Art Unit: 2133

Serial No.: 09/697,305

Examiner: Joseph D. Torres

Filed: October 27, 2000

RECEIVED

For: FAULT DETECTING METHOD AND LAYOUT METHOD

FOR SEMICONDUCTOR INTEGRATED CIRCUIT

DEC. 0 4 2003

Technology Center 2100

PETITION FOR EXTENSION OF TIME AND TRANSMITTAL OF FEE UNDER 37 C.F.R. §1.17

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

Transmitted herewith is a response to the outstanding Official Action in the above-identified application. The shortened statutory period having expired August 28, 2003, it is hereby requested that the period for response be extended for three (3) months. There is also attached our Check No. 17132 in the amount of \$950.00 in payment for:

Extension fee for response within first month pursuant to §1.136(a) (\$110.00) [\$55.00].

PLEASE ACCEPT THIS AS AUTHORIZATION TO DEBIT OR CREDIT FEES TO DEP. ACCT. 16-0331 PARKHURST & WENDEL Serial No. 09/697,305

Extension fee for response within second month pursuant to §1.136(a) (\$420.00) [\$210.00].

XX Extension fee for response within third month pursuant to \$1.136(a) (\$950.00) [\$475.00].

The Commissioner is hereby authorized to charge any additional fee (or credit any overpayment) associated with this communication to Deposit Account No. 16-0331. A duplicate copy of this letter is enclosed.

Respectfully submitted,

PARKHURST & WENDEL, L.L.P.

November 26, 2003

Date

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Attorney Docket No.: YMOR:186

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(rev. 10/03)